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					7	19, 2005	(Date)	
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APPLICATION NO.	FILING DATE	FIRST NAMED INVEN			TOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/468,746	12/21/1999 CHINTAN DES			N DESA	AI .	LSI-99-299	4042	
TITLE OF INVENTION: RECEIVE DESERIALIZER CIRCUIT FOR FRAMING PARALLEL DATA								
APPLN. TYPE	SMALL ENTITY	ISSUE FEE		Pl	JBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
	NO	\$1370			\$0	\$1370	02/07/2005	
nonprovisional	NO	\$1370	,		3 0	\$1370	02/07/2003	
EXAMINER ART U			CLASS-SUBCLASS		LASS-SUBCLASS]		
ODLAND, DAVID E 266			370-503000		370-503000	=		
1. Change of correspondence address or indication of "Fee Address" (37 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys. 1 Thelen, Reid & Pries								
	(1) the names of up to 3 registered patent attorneys 1 Thelen, Reid & Priest							
Change of correspond	or agents OR, alternatively, LLP							
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PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer				2 registered patent attorneys or agents. If no name is listed, no name will be printed.				
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LSI Logic Corporation				Milpitas, California, USA				
		ries (will not be pri	inted on the p	atent) :	☐ Individual ☐ C	orporation or other private gr	roup entity Government	
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_ ` .	MALL ENTITY status. See	•	D b. Applic	ant is no	longer claiming SMA	LL ENTITY status. See 37 C	CFR 1.27(g)(2).	
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Authorized Signature	Henry D	wy CB			Date	1/13/2007		
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